

CLAIMS

1. A method of simultaneous processing of information comprising:
 2. providing a matrix comprising a plurality of cells, each cell of the plurality of cells comprising at least one processor and at least one page, the at least one page comprising one selected from the set consisting of format counters, data pointers and process counters;
 5. connecting at least one cell to at least one area of random access memory, the area comprising at least one addressable location, and the connection being dynamically re-allocable to at least a second area of random access memory;
 8. locating instructions and registers in random access memory areas;
 9. processing information by at least one of the plurality of cells executing instructions pointed to by at least one process counter.